

Department of Electrical and Electronic Engineering

## Professor

CTS-UNINOVA  
Unit A1 (Micro & Nanoelectronics and Telecommunications)



## João Goes

Associate Professor at DEE and researcher at CTS-UNINOVA. Research interests in low-power and low-voltage analog and mixed-signal integrated circuits (RF and baseband) in advanced deep-nanoscale CMOS technologies.  
<http://docentes.fct.unl.pt/goes/>

## Objectives

Carry-out research in state-of-the-art circuits, systems and techniques targeting low-power SoC and SiP solutions in advanced CMOS technologies oriented for the wireless/wire-line communications, consumer-electronics and medical industries.

Advanced education

- M. Sc. (MIEEC, MEESC) and PhD courses (PDEEC)

Perform Technology transfer

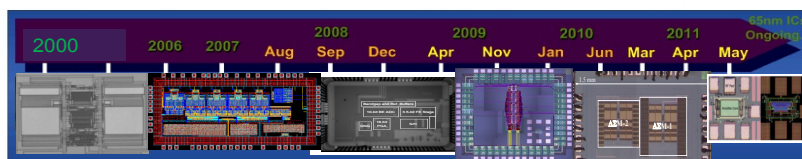
- Collaboration with national and international companies, creation of 'start-ups' and consulting work for worldwide semiconductor and electronics industry.

## Methodology

- Fundamental and Applied Research, and advanced education in collaboration with national and international Universities and research centers.
- Development of advanced integrated circuits in modern CMOS nanotechnologies and scientific support for technology transfer to the industry.

## Expected Results

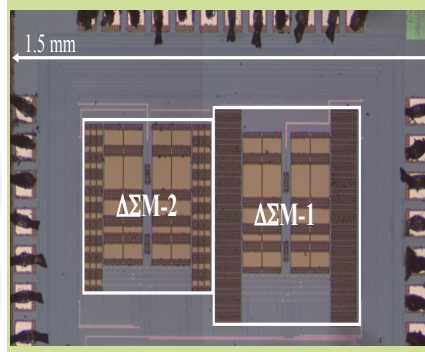
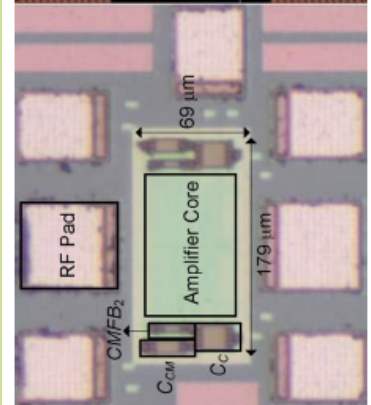
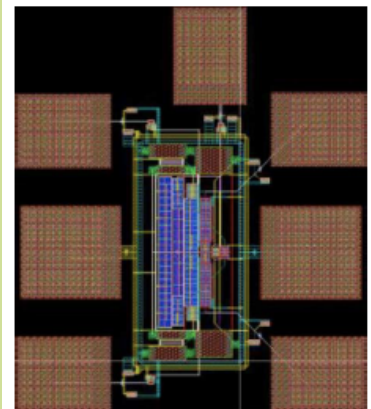
Integrated prototyping of state-of-the-art circuits and systems (SoCs) in advanced, deep-nanoscale CMOS technologies (beyond the 28nm node).



5 Books from PhD Theses



Publications	
5	Books (co-authored)
1	Books (co-edited)
10	Book Chapters
25+	Ref. Journal Articles
100+	Ref. Art. Conf. Procs.



Funding: 1.5+ Million € in Projects as PI.