

Department of Electrical and Electronic Engineering

Professor

CTS-UNINOVA
Unit A1 (Micro & Nanoelectronics and Telecommunications)



Nuno Paulino

Professor at DEE and researcher at CTS-UNINOVA. Research interests are on the design pipeline ADCs, sigma delta modulators and filter using low gain and passive circuits for advanced nanometer CMOS technologies .

http://docentes.fct.unl.pt/Nuno_F_Paulino

Objectives

Research in the following areas:

- Over sampling converters (SD modulators)
- Pipeline ADC converters
- Low power low voltage nanometer CMOS circuits
- CMOS circuits for Energy harvesting
- Optimization of circuits using genetic algorithms

Advanced education

- M. Sc. (MIEEC, MEESC) and PhD courses (PDEEC)

Perform Technology transfer

- Collaboration with national and international companies

Methodology

- Fundamental and Applied Research, and advanced education in collaboration with national and international Universities and research centers.
- Development of advanced integrated circuits in modern CMOS nanotechnologies and scientific support for technology transfer to the industry.

Expected Results

Integrated prototyping of state-of-the-art circuits and systems (SoCs) in advanced, deep-nanoscale CMOS technologies (beyond the 28nm node).

Publications in major international journals and conferences.

